

6 a constant current source coupled to said at least one reference
7 resistor, said constant current source developing a voltage across said
8 at least one reference resistor; and
9 at least one switch connected to said at least one reference
10 resistor to selectively couple said voltage to a plurality of input word
11 lines wherein the ROM device uses said plurality of data resistors to
12 interconnect said plurality of input word lines with a plurality of output
13 bit lines.

1 32. (Twice Amended) A method to maintain a current through Read-Only
2 Memory (ROM) substantially constant as temperature changes, comprising the
3 steps of:

4 supplying through at least one switch a reference voltage that is
5 responsive to changes in temperature to a plurality of input lines, wherein
6 said ROM employs a plurality of data resistors to provide electrical
7 interconnections between said plurality of input lines and a plurality of
8 output lines and said reference voltage changes to maintain said current
9 through said data resistors substantially constant.